SYSTEM TIMING

/FS

24L

FS'

LINE PULSES

25L 25L

[SH1,IC10,PIN11] OA

[SH1,IC10,PIN09] CHB

[SH1,IC10,PIN10] OB

[SH1,IC10,PIN22] DAO

[SH1,IC10,PIN37] /FS

[SH1,IC39,PIN14] E

[SH1,IC39,PIN08] DAO'

[SH1,IC39,PIN09] /NMS

[SH1,IC10,PIN28] LUM

[SH1,IC10,PIN38] /HS

[SH1,IC39,PIN07] VCLK

[SH1,IC10,PIN33] CLK

IC29B 4053 B

IC29C 4053 C

IC29A 4053 A

IC28 2318

IC17 LM1889

IC14B LS74

IC14A LS74

IC13A LS32

IC31A LS123

IC30A LS74

IC18A LS86

IC18B LS86

IC18C LS86

IC18D LS86

IC19A LS00

IC19B LS00

IC19C LS00

IC19D LS00

IC23 74LS393

IC12 74LS393

IC9A LS20

IC9B LS20

IC13C LS32

IC13B LS32

IC13D LS32

IC30B LS74

IC24 LS390

C38 C

C54 C

C49 C

C48 C

C44 C

C30 C

C29 C

C31 C

C32 2-22p

C33 C TNT

C45 C

C50 C

C51 C

C41 C

C35 C

C34 C

C36 C

C37 C

C20 C TNT

C21 C

C22 C

C24 C TNT

C25 C TNT

10K R55

10K R54

15K R60

1K R51

4.43 XL2

1K R50

R53 2K7

3K3 R52

R72 22K

1K2 R58

R59 6K8

1K R45

680R R44

R41 1K2

R42 5K6

R56 2K7

3K3 R57

R49 330R

R47 470R

R46 270R

220R R48

R43 10R

10K VR1

LK2

D22 1N4148

D21 1N4148

TR3 2N2369A

2N2369A TR2

BC212L TR4

REG1 LM7905 (TO-92)

R62 8K2

12K R63

R61 1K8

24 LINE DELAY

FS'

LINE PULSES

PIN#4 IS CONNECTED TO +5V IN THE ORIGINAL SCHEMATIC

DISCONNECT FOR B/W

DC BALANCE

LABELS D21 AND D22 APPEAR IN REVERSE ORDER IN THE ORIGINAL SCHEMATIC

SOUND

+5V

COMPOSITE

VIDEO TO MONITOR

0V

-12V

+5V

+12V

RS 232 BOX

NOTE 1 * COMPONENTS FITTED FOR MKIII PSU ONLY

DRAWN: ALFREDO CATALINA - JULY 2009		
File: Dragon 64K PAL_video_v3.sch		
Sheet: /		
Title: DRAGON C.P.U. 64k (PAL)		
Size: A3	Date: 27 may 2009	Rev:
KiCad E.D.A. EESchema (20090216-final)		Id: 1/1